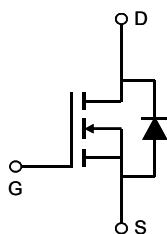
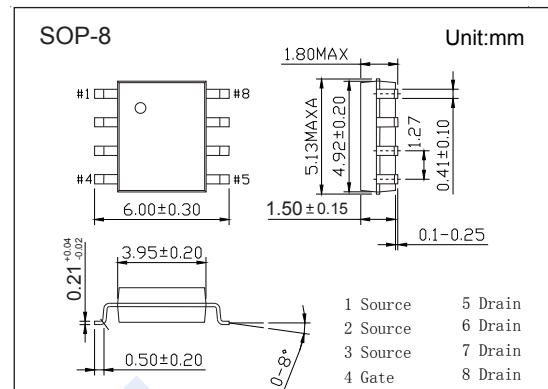


## N-Channel MOSFET

### AO4486 (KO4486)

#### ■ Features

- $V_{DS} (V) = 100V$
- $I_D = 4.2 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 79m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 90m\Omega (V_{GS} = 4.5V)$



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	4.2	A
		3.4	
Pulsed Drain Current	$I_{DM}$	31	
Avalanche Current	$I_{AS}, I_{AR}$	14	
Avalanche Energy	$E_{AS}, E_{AR}$	10	mJ
Power Dissipation	$P_D$	3.1	W
		2	
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	40	°C/W
		75	
Thermal Resistance.Junction- to-Lead	$R_{thJL}$	24	
Junction Temperature	$T_J$	150	
Storage Temperature Range	$T_{stg}$	-55 to 150	°C

## N-Channel MOSFET

### AO4486 (KO4486)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μ A, V <sub>GS</sub> =0V	100			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>D</sub> =100V, V <sub>GS</sub> =0V			1	uA
		V <sub>D</sub> =100V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>D</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>D</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.6		2.7	V
Static Drain-Source On-Resistance	R <sub>D(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3A			79	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =3A T <sub>J</sub> =125°C			151	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A			90	
On State Drain Current	I <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =5V	31			A
Forward Transconductance	g <sub>FS</sub>	V <sub>D</sub> =5V, I <sub>D</sub> =3A		20		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>D</sub> =50V, f=1MHz	620		942	pF
Output Capacitance	C <sub>oss</sub>		38		81	
Reverse Transfer Capacitance	C <sub>rss</sub>		13		35	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>D</sub> =0V, f=1MHz	0.7		2.2	Ω
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =50V, I <sub>D</sub> =3A	13		20	nC
Total Gate Charge (4.5V)			6.4		10	
Gate Source Charge	Q <sub>gs</sub>		2.2		3.4	
Gate Drain Charge	Q <sub>gd</sub>		2.4		5.8	
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =50V, R <sub>L</sub> =16.7Ω, R <sub>GEN</sub> =3Ω		6		ns
Turn-On Rise Time	t <sub>r</sub>			2.5		
Turn-Off DelayTime	t <sub>d(off)</sub>			21		
Turn-Off Fall Time	t <sub>f</sub>			2.4		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 3A, dI/dt= 500A/us	14		28	nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		65		123	
Maximum Body-Diode Continuous Current	I <sub>s</sub>				3.5	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>s</sub> =1A, V <sub>GS</sub> =0V			1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 us pulses, duty cycle 0.5% max.

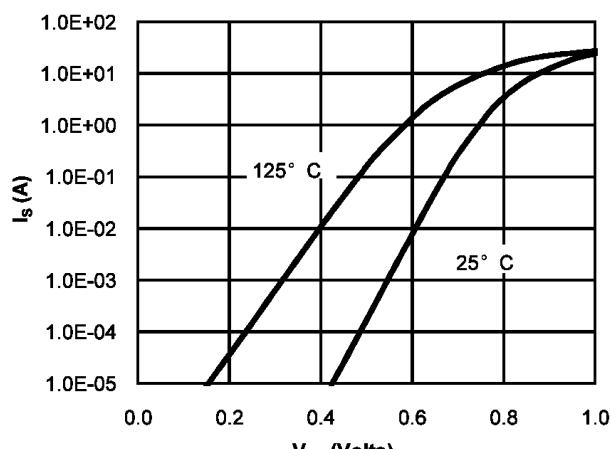
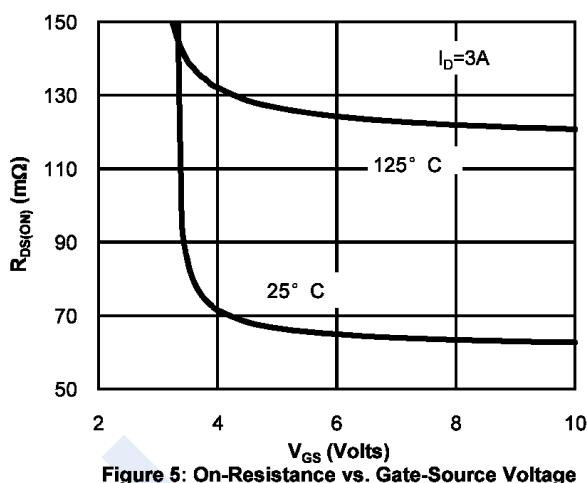
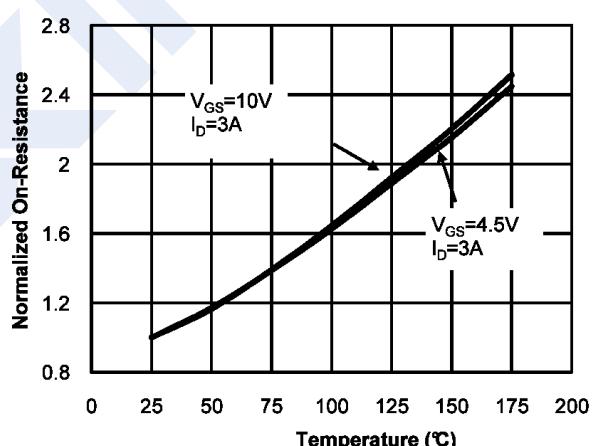
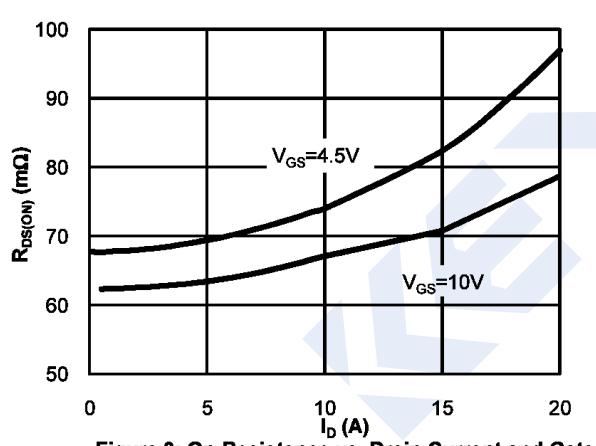
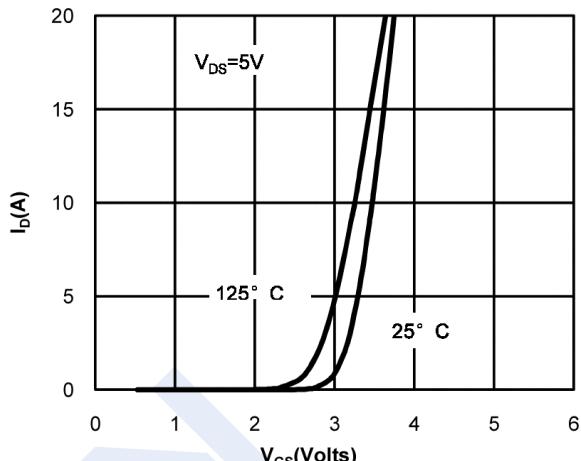
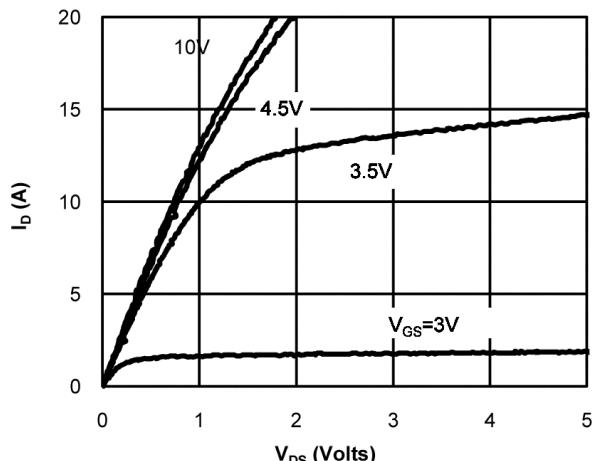
■ Marking

Marking	4486 KC***
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## N-Channel MOSFET

### AO4486 (KO4486)

#### ■ Typical Characteristics



## N-Channel MOSFET

### AO4486 (KO4486)

#### ■ Typical Characteristics

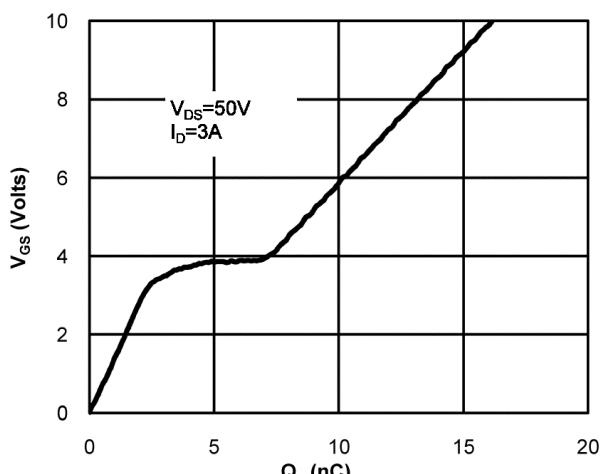


Figure 7: Gate-Charge Characteristics

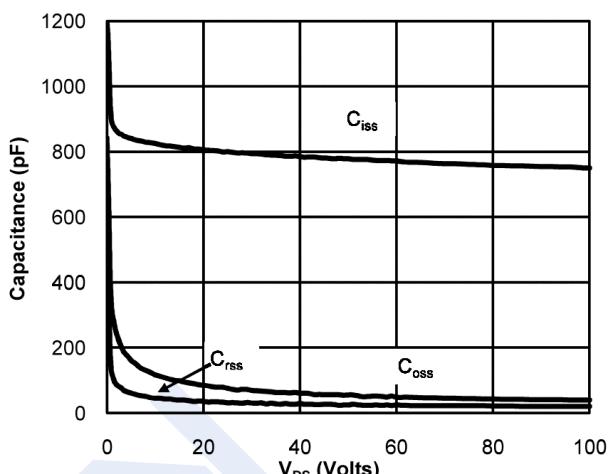


Figure 8: Capacitance Characteristics

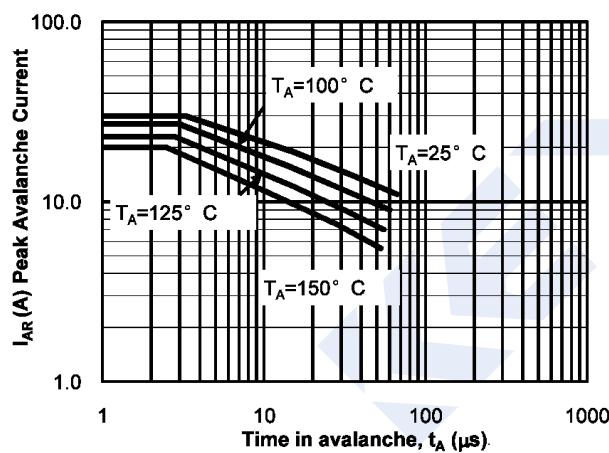


Figure 9: Single Pulse Avalanche capability (Note C)

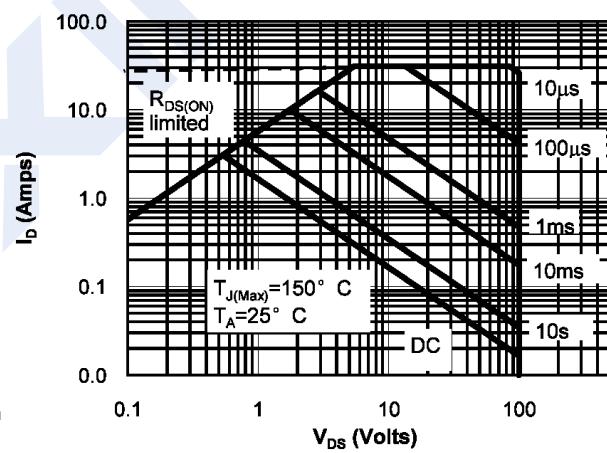


Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

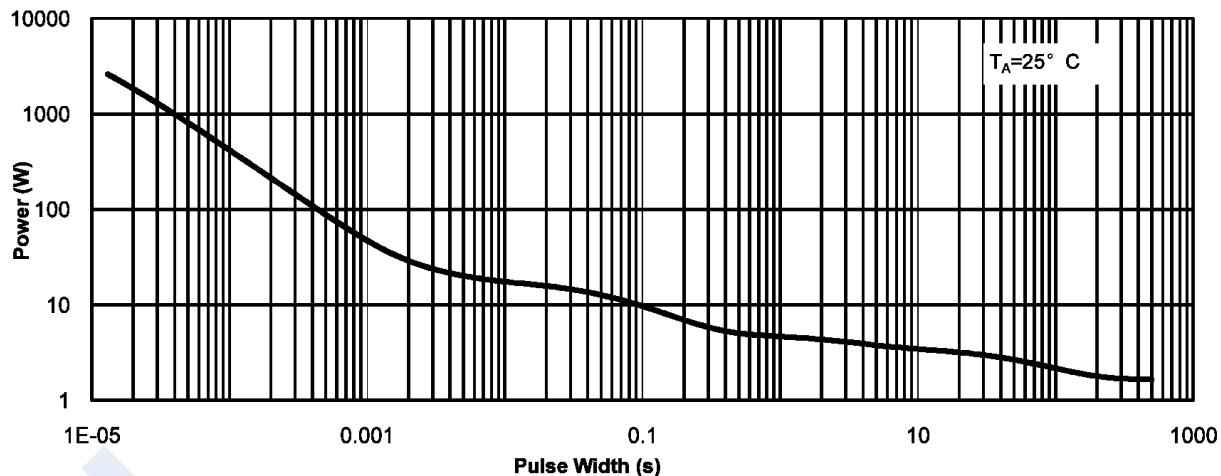


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)

**N-Channel MOSFET****AO4486 (KO4486)**

## ■ Typical Characteristics

